Figure 1

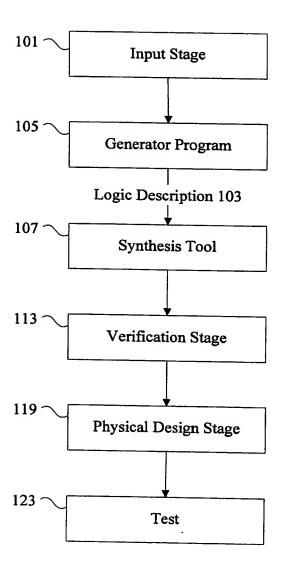


Figure 2

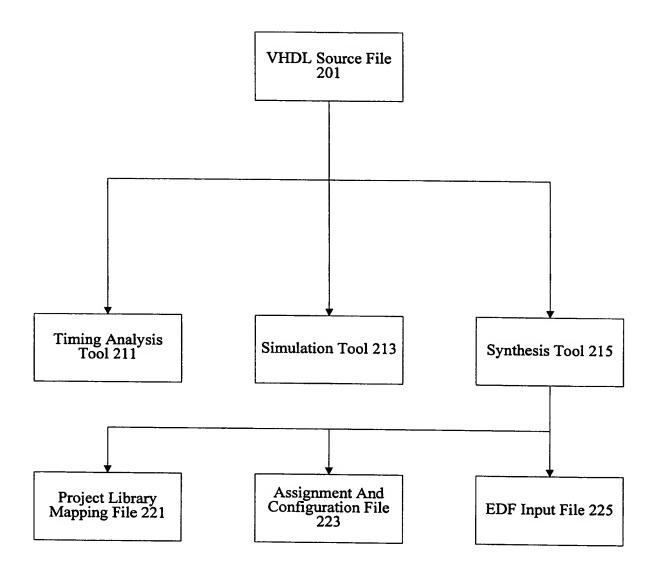
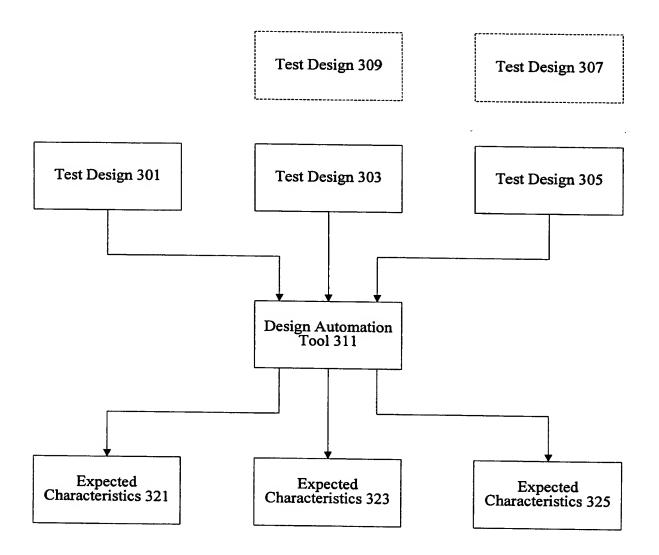


Figure 3



7 423 Register 443 Register 447 Processor Core 463 DSP Core 461 Register 441 Register 445 Figure 4 Memory 433 Adder 453 Timer 457 Top Level Module 401 Memory 431 PLL 451 Filter 455 417 415 411 413

425

427

421

Figure 5

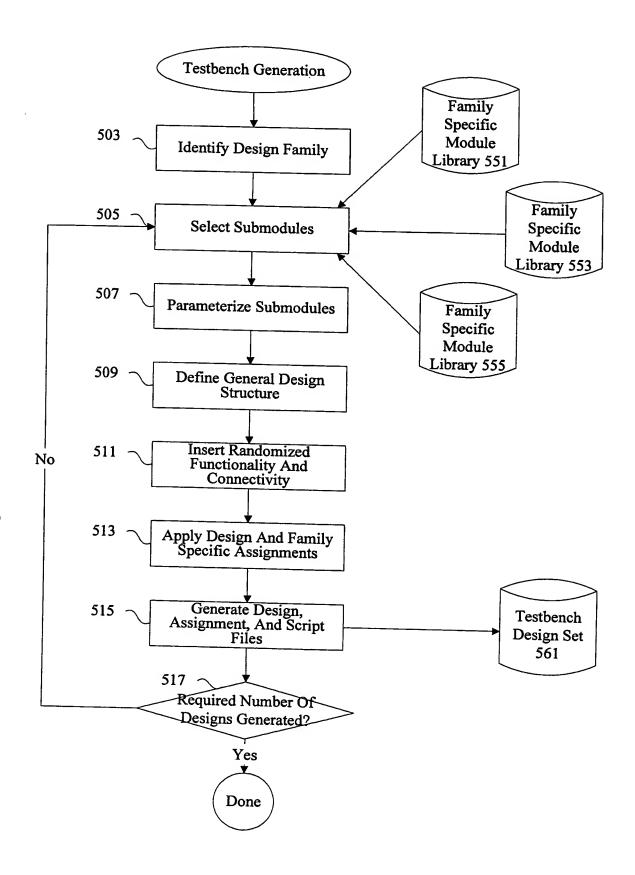


Figure 6

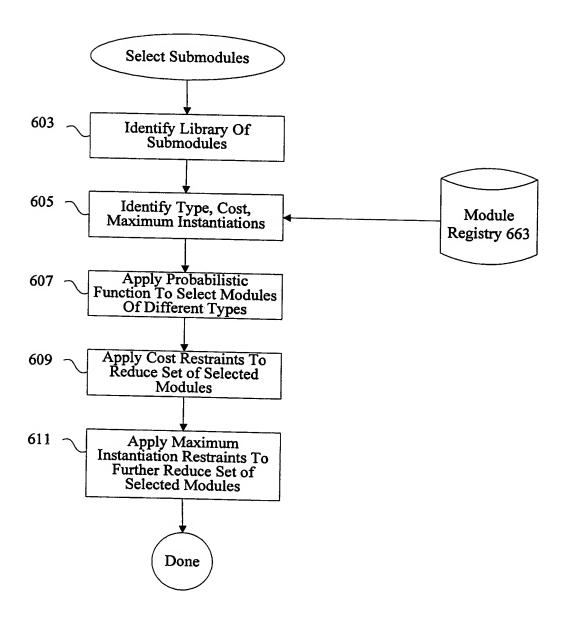


Figure 7

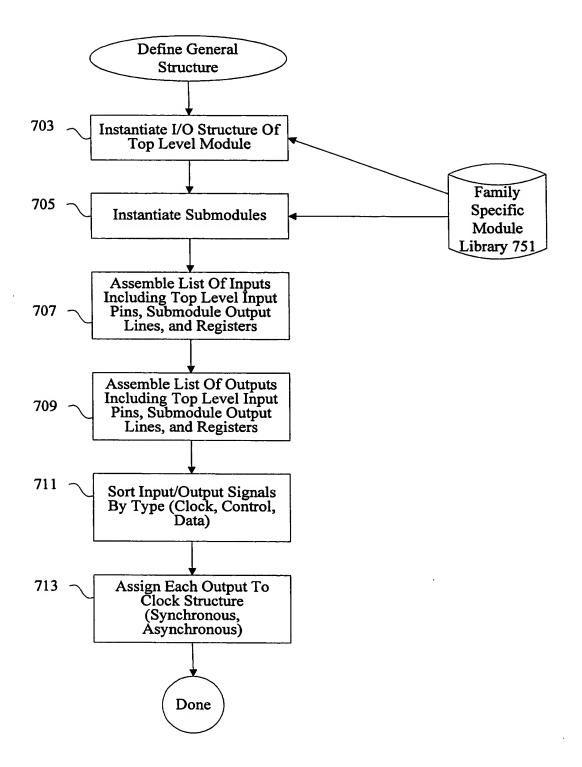


Figure 8

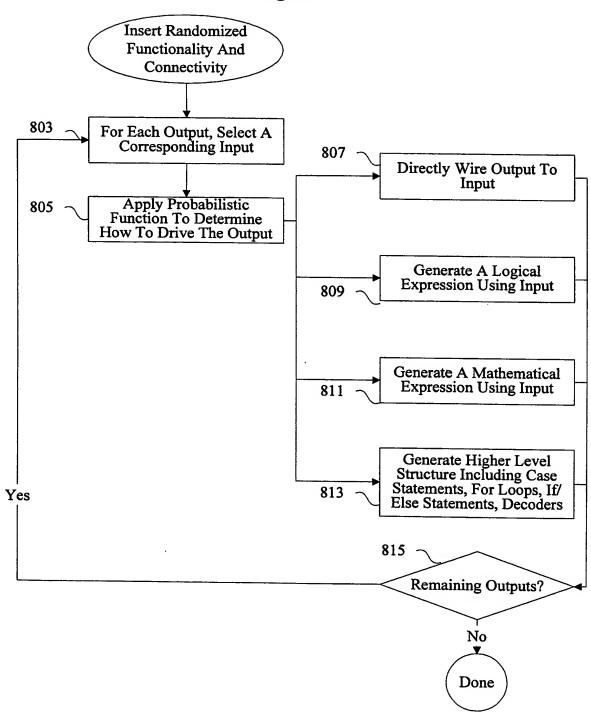


Figure 9

